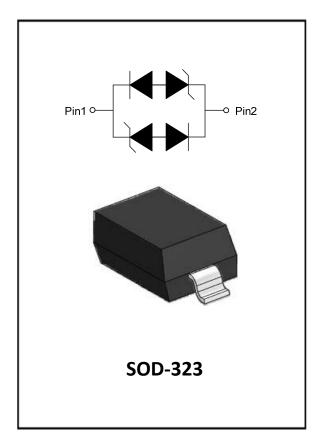




1-Line, Bi-directional, Ultra-low Capacitance, Transient Voltage Suppressor



Features

- Stand-off voltage: 3.3V Max
- Transient protection for each line according to IEC61000-4-2(ESD): ±30kV (contact) IEC61000-4-5(surge): 21A (8/20µs)
- Ultra-low capacitance: C_J = 1.5 pF typ
- Low leakage current
- Low clamping voltage
- RoHS Compliant

Applications

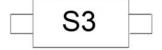
- Cellular Handsets and Accessories
- Display Ports
- MDDI Ports
- USB Ports
- Video graphics cards
- Digital Video Interface (DVI)
- PCI Express and Serial SATA Ports

Caution:

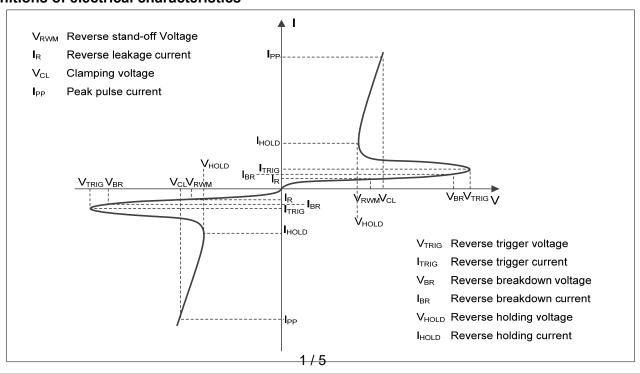
This Device is designed for signal line protection only. Not intended to be used under bias, not for application with a power line.

Mechanical Data

- •Lead Finish: Matte Tin
- •Case Material: "Green" Molding Compound.
- •Moisture Sensitivity: Level 1 per J-STD-020
- •Marking Information: See Below



■Definitions of electrical characteristics





SESDSLC3V3D3B

■Maximum Ratings

PARAMETER	SYMBOL	LIMITS	UNIT	
Peak pulse power (t _p = 8/20μs)	P_{pk}	312	W	
ak pulse current ($t_p = 8/20\mu s$)		26	А	
ESD according to IEC61000-4-2 air discharge		±30	KV	
ESD according to IEC61000-4-2 contact discharge	V_{ESD}	±30		
Junction temperature	T₃	-55~125	°C	
Storage temperature	T _{STG}	-55~150	°C	

■Electrical Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	Symbol	UNIT	Conditions	Min	Тур	Max
Reverse maximum working voltage	V_{RWM}	V	Any I/O pin to ground			3.3
Reverse leakage current	I _R	μΑ	V _{RWM} = 3.3V, any I/O pin to ground			1
Reverse breakdown voltage	$V_{(BR)}$	V	I _T =2μA,	3.5		
Reverse holding voltage	V _{HOLD}	V	I _{HOLD} = 50mA,	0.8		
Clamping voltage 1)	V _{CL}	V	I _{PP} = 16A, t _p = 100ns		5.4	
Olympia marka ma 2)	V _{CL}	V	I _{PP} = 1A, t _p = 8/20μs			5
Clamping voltage ²⁾		V	$I_{PP} = 26A, t_p = 8/20 \mu s$			12
Junction capacitance	CJ	pF	V _R = 0V, f = 1MHz		1.5	

Notes:

- 1) TLP parameter: Z_0 = 50 Ω , t_p = 100ns, t_r = 2ns, averaging window from 60ns to 80ns.
- 2) Non-repetitive current pulse, according to IEC61000-4-5.

■Ordering Information (Example)

PREFERED P/N	PACKING CODE	UNIT WEIGHT(mg)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
SESDSLC3V3D3B	F2	Approximate 4.4	3000	30000	120000	7 reel



■ Characteristics (Typical)

Fig.1 8/20µs waveform per IEC61000-4-5

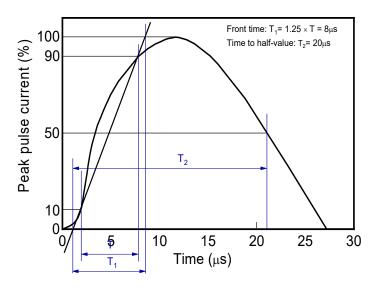


Fig.3 Clamping voltage vs. Peak pulse current

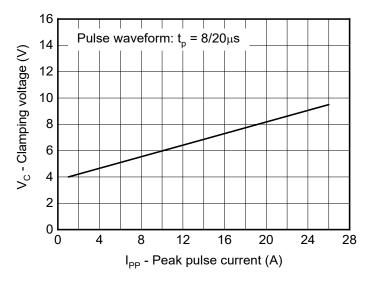


Fig.5 Non-repetitive peak pulse power vs. Pulse time

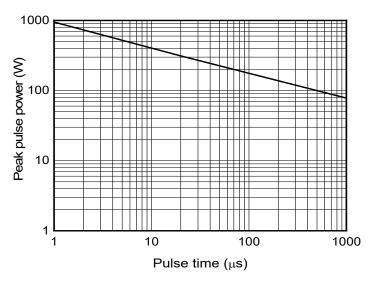


Fig.2 Contact discharge current waveform per IEC61000-4-2

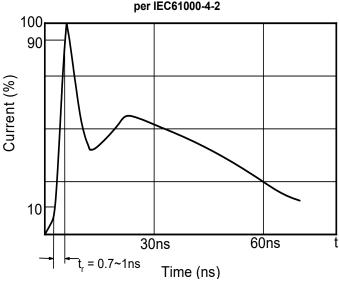


Fig.4 Capacitance vs. Reverse voltage

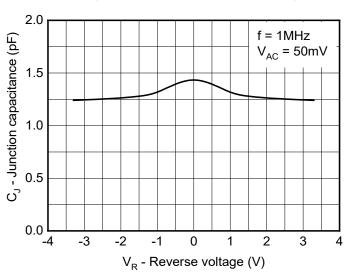
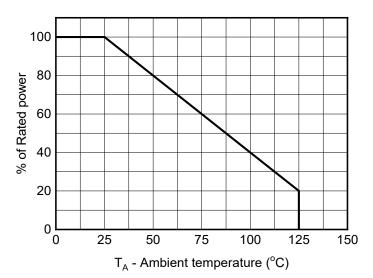
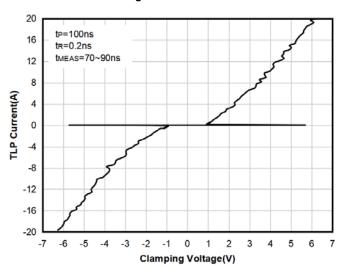


Fig.6 Power derating vs. Ambient temperature

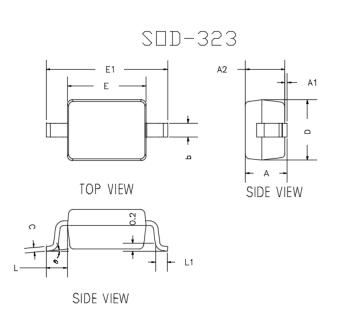


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Fig.7 TLP Measurement

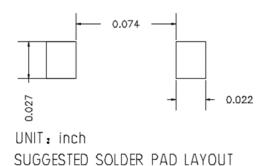


■ Outline Dimensions



DIMENSIONS					
DIM	INC	HES	ММ		
	MIN	MAX	MN	MAX	
А	THE MOST BASE	0.0393	was salar datar	1.0000	
AI	0.0000	0.0039	0.0000	0.1000	
A2	0.0314	0.0354	0.8000	0.9000	
lo	0.0098	0.0157	0.2500	0.4000	
с	0.0031	0.0059	0.0800	0.1500	
D	0.0472	0.0551	1,2000	1.4000	
E	0.0629	0.0709	1,6000	1.8000	
El	0.0984	0.1063	2.5000	2.7000	
L	0.0187TYP		0.475TYP		
L1	0.0098	0.0157	0.250	0.400	
θ	0*	8*	0.	8*	

■ Soldering Footprint



Notes.

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.



SESDSLC3V3D3B

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