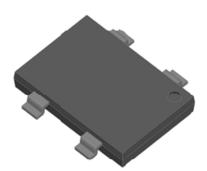
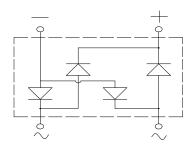




# **Low VF Bridge Rectifiers**





#### **Features**

- UL recognition, file #E313149
- Glass passivated chip junction
- Ideal for automated placement
- High surge current capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

#### **Typical Applications**

General purpose use in AC/DC bridge full wave rectification for SMPS, lighting ballaster, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

#### **Mechanical Data**

• Package: YBS3

Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, Halogen-free

• Terminals: Tin plated leads, solderable per

J-STD-002 and JESD22-B102
• Polarity: As marked on body

# ■Maximum Ratings (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	YBSML8008
Device marking code			YBSML8008
Maximum Repetitive Peak Reverse Voltage	VRRM	V	800
Maximum RMS Voltage	VRMS	V	560
Maximum DC blocking Voltage	VDC	V	800
Average rectified output current @60Hz sine wave, R-load, Tc=105°C	Io	Α	8.0
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave,1 cycle, Tj=25°C	IFSM	А	250
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25°C			500
Current squared time @1ms≤t≤8.3ms Tj=25°C,Rating of per diode	l²t	A <sup>2</sup> s	259.4
Storage temperature	T <sub>stg</sub>	°C	-55 ~ <b>+</b> 150
Junction temperature	Tj	°C	-55 ~ <b>+</b> 150

## **■Electrical Characteristics** (Ta=25°C Unless otherwise specified)

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PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	Min	Тур	Max
Instantaneous forward voltage drop per diode	VF	٧	IFM=4.0A	0.7	0.875	0.92
DC reverse current at rated DC blocking voltage per diode	lR	μА	T <sub>j</sub> =25°C	-	0.18	5
			Tj =125°C	-	42	100
Junction capacitance	Cj	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	35	74	150

### YBSML8008

### **Thermal Characteristics** $(T_a=25^{\circ}\mathbb{C} \text{ Unless otherwise specified})$

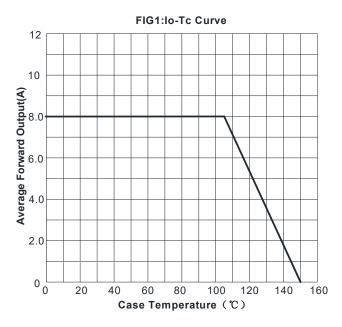
	PARAMETER	SYMBOL	UNIT	YBSML8008
	Between Junction and Ambient	ent R <sub>eJ-A</sub>	°C/W	55
Typical Thermal Resistance	Between Junction and Lead	R <sub>θJ-L</sub>		10
	Between Junction and Case	R <sub>θJ-C</sub>		5

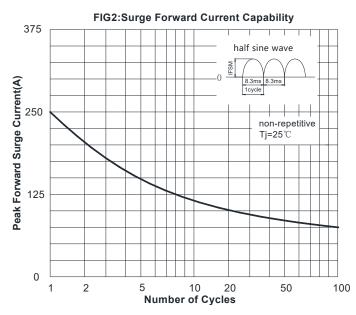
Note: Device mounted on P.C.B with 35mm\*25mm\*1.7mm.

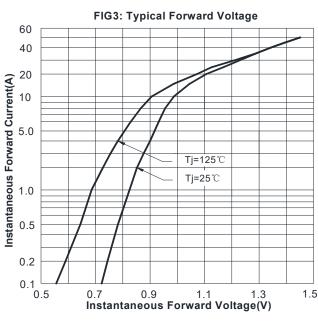
**■Ordering Information** (Example)

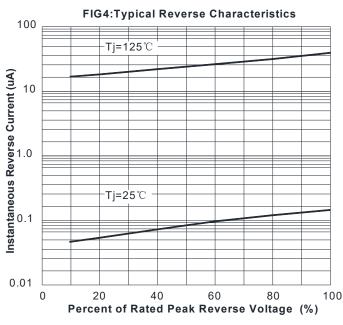
PREFERED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YBSML8008	F1	Approximate 0.38	1800	/	25200	13" Reel

#### **■ Characteristics** (Typical)





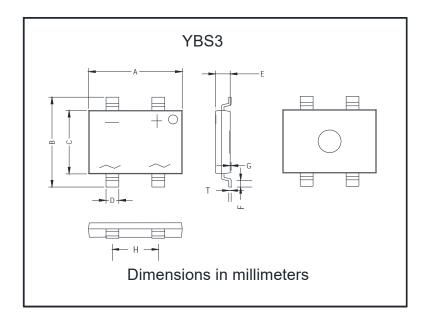






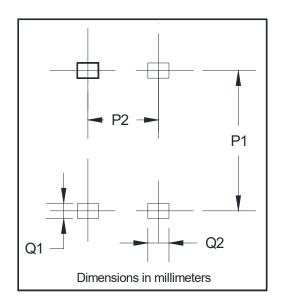


#### **■ Outline Dimensions**



YBS3					
Dim	Min	Max			
Α	10.00	10.40			
В	9.70	10.10			
С	6.80	7.20			
D	1.3	1.5			
E	1.4	1.8			
F	0.5	1.1			
G	0	0.15			
Ι	4.9	5.1			
T	0.20	0.30			

# ■ Suggested pad layout



YBS3		
Dim Min		
P1	9.25	
P2	5.00	
Q1	1.00	
02	1.5	



### YBSML8008

#### **Disclaimer**

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